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J1093 U.S. PTO

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PATENT

APPLICATION FOR U.S. PATENT
TRANSMITTAL FORM

THE COMMISSIONER OF PATENTS
AND TRADEMARKS
Washington, D.C. 20231

Docket No. 1138-EP

Sir:

Transmitted herewith for filing is the patent application of:

Inventor(s): Sanjay Ramakrishna PILLAY and Raghunath Krishna RAO

For: **CIRCUITS AND METHODS FOR DEBUGGING AN EMBEDDED PROCESSOR
AND SYSTEMS USING THE SAME**


Enclosed are: 57 Sheets of Drawings

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LARGE ENTITY FEE CALCULATION					FEE
	Number		Number Extra	Rate	Basic Fee \$ 710.00
Total Claims	20	- 20 =	0	x \$18=	\$ 0 .00
Independent Claims:	3	- 3 =	0	x \$80=	\$ 0.00
Multiple Dependent Claims:	N/A	N/A	N/A	N/A	\$ N/A
Total Filing Fee =					\$ 710.00

Enclosed is a check in the amount of \$ 710.00. Please charge any additional fees or credit any overpayment to Deposit Account No. 23-2426 of Winstead Sechrest & Minick, P.C. A duplicate copy of this sheet is enclosed.

Date March 30, 2001


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Date of Deposit March 30, 2001

This 280 pages of Application, 57 pages of Drawings, executed Declaration and Power of Attorney along with the PTO Filing Fee Transmittal sheet and check in the amount of \$ 710.00 application fees are being deposited with sufficient postage with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. 1.10 on the date indicated above and is addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231.


S. Taylor Lenz

Signed March 30, 2001

**CIRCUITS AND METHODS FOR DEBUGGING AN EMBEDDED PROCESSOR
AND SYSTEMS USING THE SAME**

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